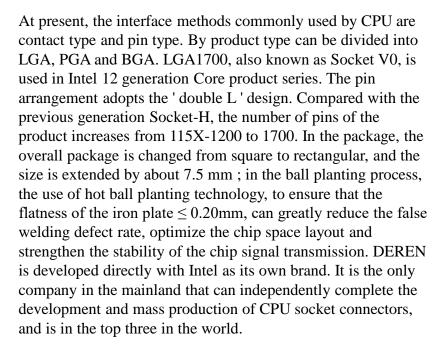
CPU SOCKET PRODUCT FAMILY LGA 1700 SOCKET-V0

RELEVANT INFORMATION

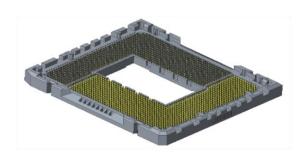


- An array electrode contact package is made on the bottom of the chip, which can accommodate more input and output pins in a smaller package.
- Total number of contacts for LGA 1700, 0.80mm orthogonal.
- Support diverse latch designs to meet customer different application fields.
- This product defines surface mount (Land Grid Array) socket

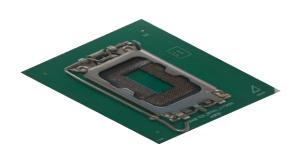
FETURE

- Data rate transfer up to 32GT/s.
- Hot Ball-Attach Process.
- Static Pre-load compressive range ILM at 400.5N ~ 845.5N.25.
- Finish: Au15" Plating in contact area and 50u"min Ni plating over all.
- Operating temperature : -40 $^{\circ}$ C to 100 $^{\circ}$ C.





Socket



Product

TARGET MARKET

■ Applies to Intel 12generation Core processors.

BENEFIT

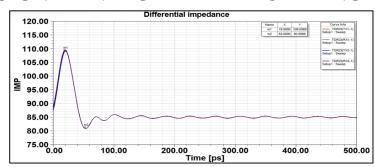
- Meet Intel Design guidelines 1.0.
- Meet transmission efficiency.
- Fits for customer process.

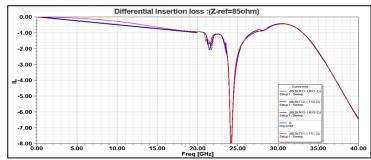
CPU SOCKET PRODUCT FAMILY LGA 1700 SOCKET-V0

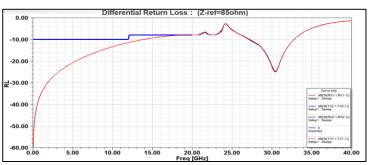
DEDEN

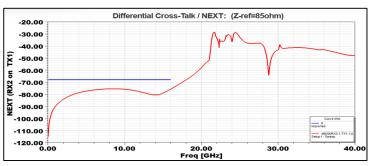
TECHNICAL INFORMATION

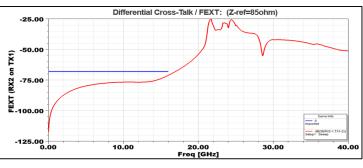
SIGNAL INTEGRITY PERFORMANCE











MECHANICAL PERFORMANCE

- Static Pre-Load Compressive: 166.6N-416.5N.
- Static Compressive per Contact:10gf-25gf.
- Durability: 30times.

ELECTRICAL PERFORMANCE

- Average Low level contact resistance : $\leq 27 \text{m}\Omega$.
- Insulation resistance: 800 M Ohms.
- Dielectric withstanding voltage: 360 Volts RMS.
- Current rating: 0.5A/pin De-rate.

ENVIRONMENTAL

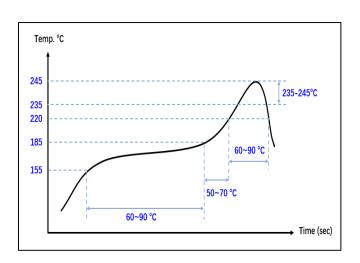
- Flammability: UL 94 V-0.
- Low halogen: 900ppm max. Cl, 900ppm max Br.
- Compliant with RoHS directive 2011/65/EU.
- Operating Temperature: -40° C to $+100^{\circ}$ C.

SPECIFICATION

- SPEC: Intel Design guidelines 1.0.
- DEREN PRODUCT SPEC: DR-PS-0128.

APPLICATION SPEC

■ Reflow temperature profile:



■ Recommended stencil thickness: 0.13mm.

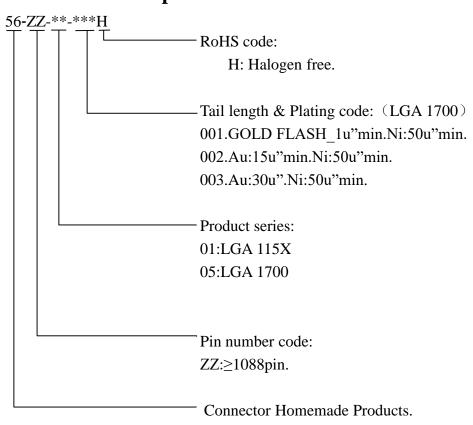
http://www.deren.com.cn E-mail: sales@deren.com Tel:0755-3326000

CPU SOCKET PRODUCT FAMILY LGA 1700 SOCKET-V0



PART NUMBERS & STRUCTRUE

Part Number Description:



Product Structure

