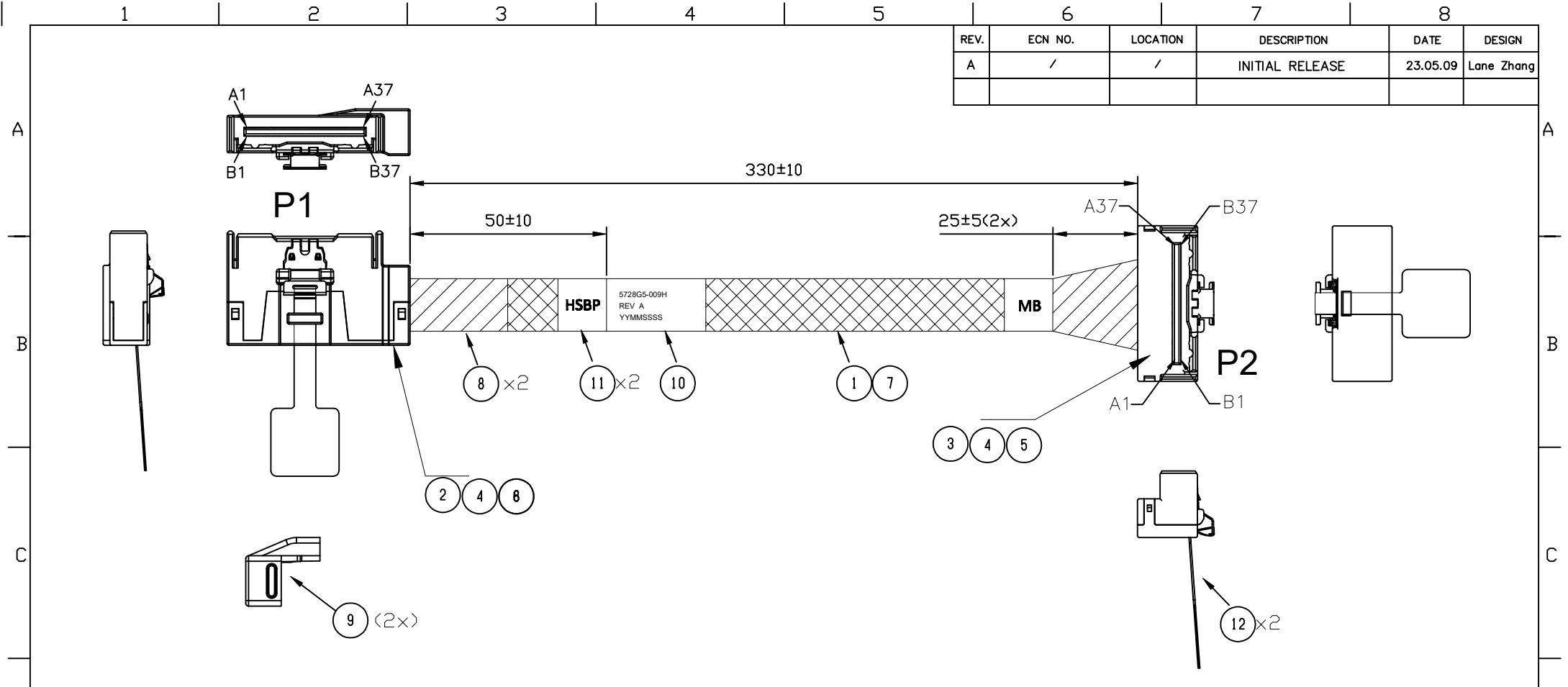



REV.	ECN NO.	LOCATION	DESCRIPTION	DATE	DESIGN
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12	PULL TAB	W/PULL TAB LATCH FOR SLIMSAS
11	LABEL	W=15mm, T=0.08mm, WHITE
10	LABEL	W=30mm, T=0.08mm, WHITE
9	DUST COVER	MCIO 8X DUST COVER, MATERIAL:PE
8	ACETATE TAPE	ACETATE, BLACK
7	WEAVE TUBE	PET, BLACK SINGLE ROOT
6	COVER	MCIO 8X LS, BLACK
5	COVER	MCIO 8X RA, BLACK
4	LATCH	SUS301 3/4H, T=0.25mm
3	PCB	MCIO 74P RA;Au 30U'' ;85ohm
2	PCB	MCIO 74P LS;Au 30U'' ;85ohm
1	Raw cable	PCIE 5.0;30AWGx1P+2D+AM;85 SLIVERED
No.	PART NAME	DESCRIPTION

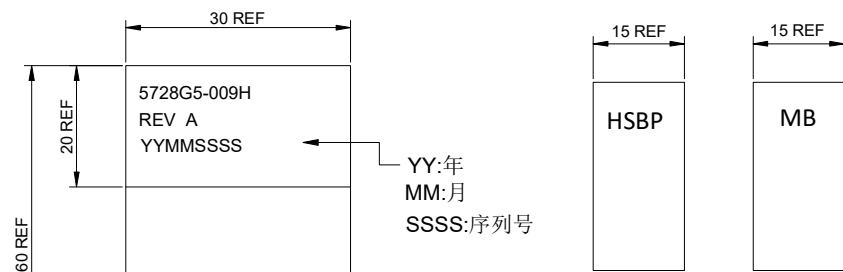
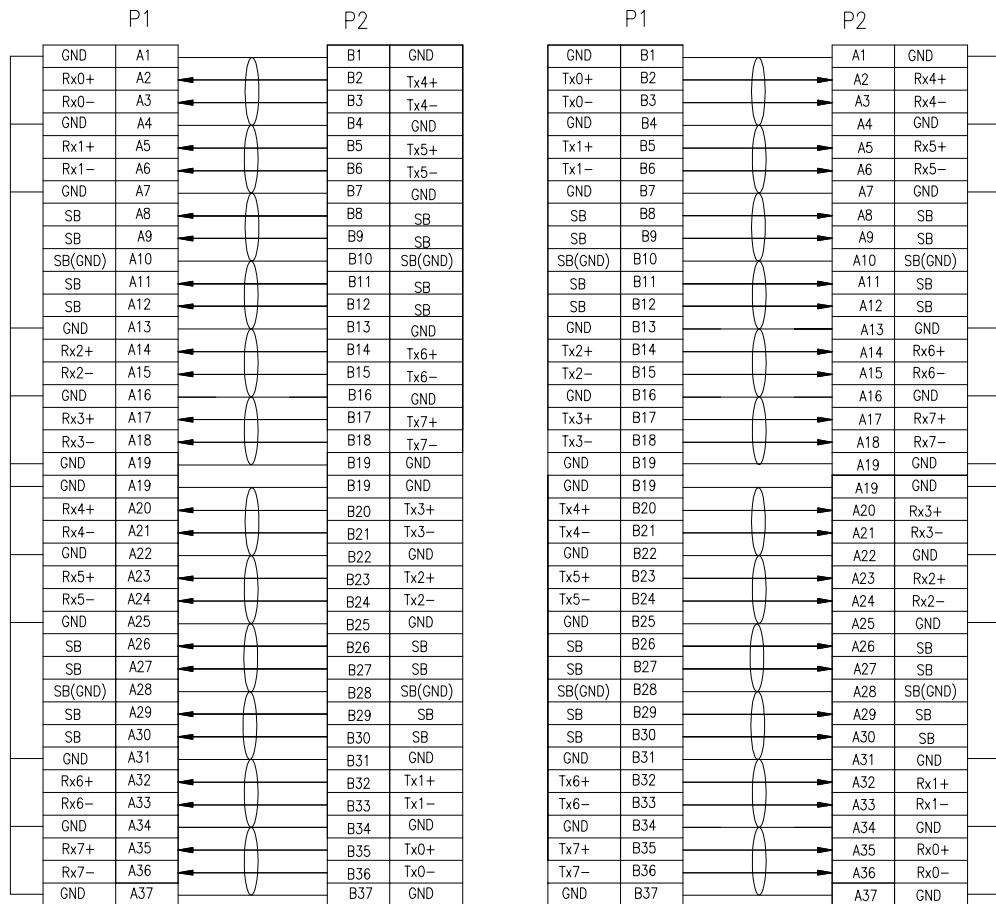
Notes:


- ELECTRICAL PERFORMANCE:
 - 1-1. 100% OPEN & SHORT TEST.
 - 1-2. CONNECTION RESISTANCE: 3.0 Ohm MAX
 - 1-3. INSULATION RESISTANCE: 10M Ohm/DC300V/10ms (MIN)
- THE IMPEDANCE IS CABLE AREA : 85±10% OHM, CONNECTOR AREA: 85±15% OHM.
- THE CABLE ASSEMBLY ELECTRICAL AND MECHANICAL PERFORMANCE MUST BE MEET PCIE GEN5.0
- THE CABLE ASSEMBLY SHALL BE MEET RoHS2.0 COMPLIANT

<table border="1"> <thead> <tr> <th>DIM</th> <th>TOL</th> <th>DIM</th> <th>TOL</th> </tr> </thead> <tbody> <tr> <td>x</td> <td>±0.15</td> <td>x</td> <td>±1*</td> </tr> <tr> <td>.x</td> <td>±0.10</td> <td>.xx</td> <td>±0.5*</td> </tr> <tr> <td>.xx</td> <td>±0.05</td> <td></td> <td></td> </tr> </tbody> </table>	DIM	TOL	DIM	TOL	x	±0.15	x	±1*	.x	±0.10	.xx	±0.5*	.xx	±0.05			 <p>DEREN SHENZHEN DEREN ELECTRONIC CO.,LTD</p>	INSPECTION DRAWING		DATE	TITLE: NVME MCIO 8X CABLE
	DIM	TOL	DIM	TOL																	
x	±0.15	x	±1*																		
.x	±0.10	.xx	±0.5*																		
.xx	±0.05																				
DRAW NO.	DESIGN:			Customer :																	
UNIT: mm	CHECK:			Customer P/N: T9333310003516																	
Page: 1 of 2	APPROVAL:			DEREN P/N: 5728GS-009H																	

REV.	ECN NO.	LOCATION	DESCRIPTION	DATE	DESIGN
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WIRING TABLE:



DIM	TOL	DIM	TOL	 DEREN SHENZHEN DEREN ELECTRONIC CO.,LTD	INSPECTION DRAWING	DATE	TITLE: NVME M.2 8X CABLE	
x	±0.15	x	±1*		DRAW NO.	DESIGN:		Customer :
.x	±0.10	.xx	±0.5*			UNIT: mm	CHECK:	
.xx	±0.05				Page: 2 of 2	APPROVAL:		DEREN P/N: 5728G5-009H
				REV. A				