

GENERAL PROFILE

The Riser cable mainly consists of PCIe connector and MCIO connector, and adopts cable-board integrated solution. The differential signal, power, low speed and other signals are welded with the on-board pad, and the cable is still matched with the mainboard end connector using the traditional interconnection scheme (such as slimsas, Oculink, MCIO, micro-fit, etc.) With the advantages of reducing signal attenuation, improving signal integrity, and better space use efficiency, it is more widely used in application scenarios with high requirements for high-speed transmission efficiency.

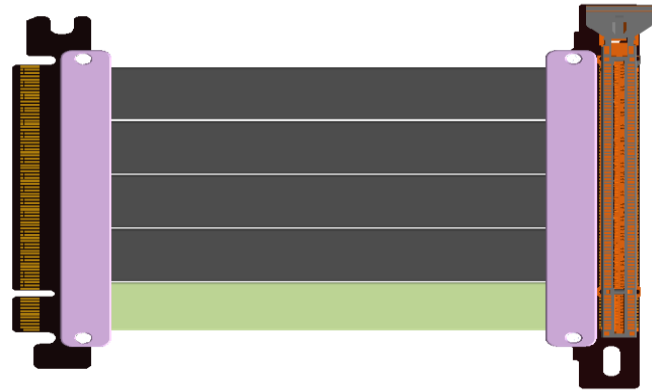
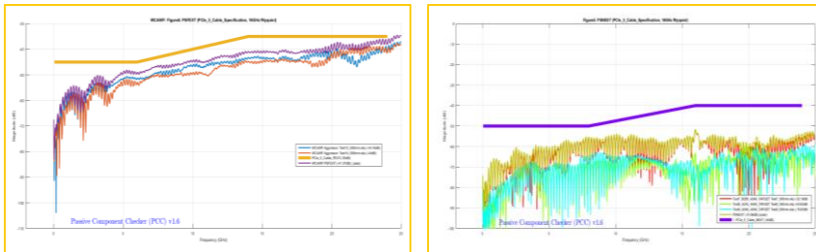
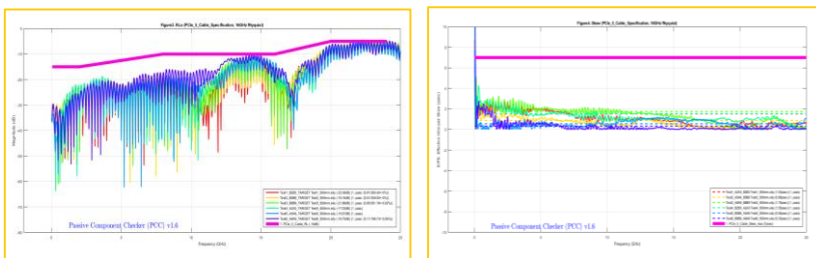
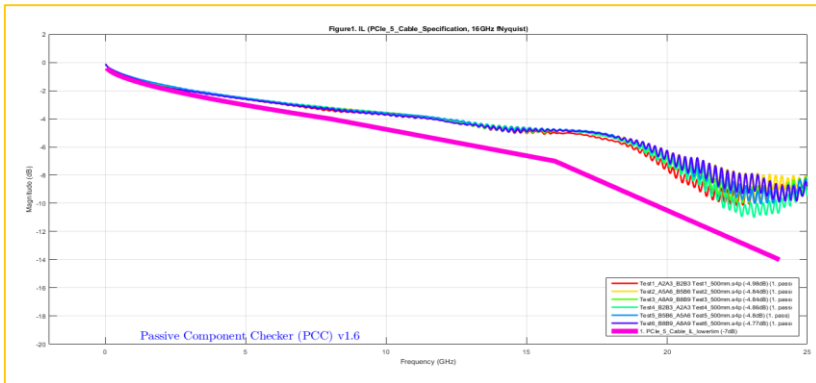
TARGET MARKET

- High performance Computer
- Server, Switch
- Data Center, network device

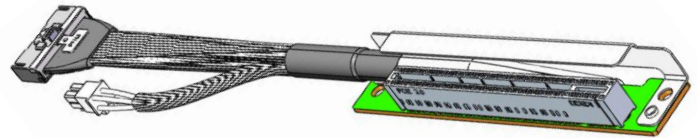
TECHNICAL INFORMATION

- Excellent SI performance, support PCIe Gen5 and expandable to PCIe Gen6.
- SI has high consistency and stability.
- Support PCIe 5.0 for long distance applications.
- Customer system architecture can be more flexible design.
- Reduce client PCBA cost.
- The welding process is compatible with laser welding and resistance welding, and the quality is repeatable.
- Support motherboard to expansion card applications..

SI PERFORMANCE



PCIE Cable



Riser Cable

MECHANICAL PERFORMANCE

- Mating Force: 55N maximum
- Un-mating Force: 49N maximum
- Rated durability cycles: 200 cycles minimum.

ELECTRICAL PERFORMANCE

- Low level contact resistance: 30mΩ
- Insulation resistance: 10MΩ min
- Dielectric withstanding voltage: 250VAC
- Current rating: 0.5A/pin

ENVIRONMENTAL

- Flammability: VW-1, 94V-0
- Compliant with RoHS 2011/65/EU
- Operating Temperature: -20° C to + 80° C

JEDEC SPEC:

Industry specification SFF-TA-1016

DEREN PRODUCT SPEC:

DR-PS-0088