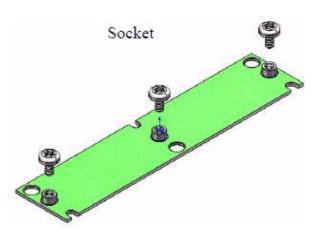
LPDDR5 CAMM2 LGA666 CONN PRODUCT

RELEVANT INFORMATION

LP5 CAHH2 LGA666 (Successor to the previous LGA644)

- Based on the LGA644 design, 22 additional pins have been incorporated to optimize the routing of the module card and reduce signal integrity (SI) interference.
- The traces on the mainboard for the RAM to communicate the CPU—can be much shorter with CAMM2 compared to DIMM. Shorter electrical paths require less power and can rehigher speeds.
- The signal pins of SO DIMM are isolated by GND pins, but the signal pins of CAMM2 are surrounded by GND pins, which will have better X-talk performance..
- CAMM2 has much lower Z-height than SO-DIMM. SO-DIMM modules are taller, especially when you stagger more than one, taking up crucial space that can be used for cooling or a different board layout.
- The reduced footprint on the PCB underneath the CAMM2 module also saves valuable space for other motherboard components.





Product

FETURE

- Voltage: 25 Volts AC (rms).Hot Ball-Attach Process.
- Recommended torque for screw fastening:0.1~0.2 N.m (14.2~28.3 oz.in).
- Normal Force Terminal (fully compressed): Max 35 gf per pin.
- Retention Force -Terminal: 50 gf minimum per pin; maximum movement of contact of 0.38 mm.
- Continuous Temperature: 15 °Cto 85 °C.
- Operating temperature : -55 °C to 85 °C.

TARGET MARKET

■ LPDDR5 CAMM2 is aimed at highperformance notebooks

BENEFIT

- Meet JEDEC standard.
- Meet transmission efficiency.
- Fits for customer process.

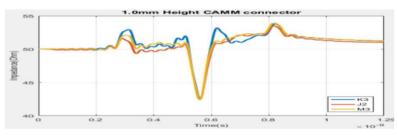
http://www.deren.com.cn E-mail: sales@deren.com Tel:0755-3326000

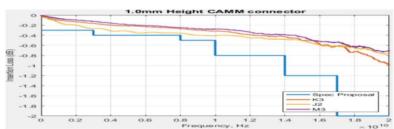
LPDDR5 CAMM2 LGA666 CONN PRODUCT

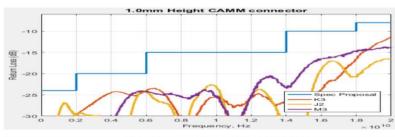


TECHNICAL INFORMATION

SIGNAL INTEGRITY PERFORMANCE

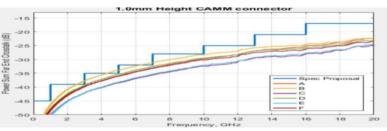












MECHANICAL PERFORMANCE

- Normal Force: max35 g-f per pin
- Retention Force Terminal : min 50 g-f per pin
- Durability: 25times.
- Dye and Pry: <25% solder joint crack
- Recommended fastening torque (mounting):
- 0.1~0.2 N.m (14.2~28.3 oz.in)
- Recommended fastening retention (un-mounting): 0.08 N.m. (11.3 oz.in) minimum

ELECTRICAL PERFORMANCE

- Low level contact resistance :Initial single pin 50mΩ max. after reliability test and initial LLCR fluctuations of 20mΩ max.
- Dielectric withstanding voltage: 500VAC RMS.
- Current rating: 1.0A/pin De-rate.
- Insulation resistance: 250 MΩ Min. (Initial) \times 100 MΩ Min. (Final)
- Voltage: 25 Volts AC (rms)

ENVIRONMENTAL

- Flammability: UL 94 V-0.
- Compliant with RoHS directive 2011/65/EU.

SPECIFICATION

- SPEC: Meet JEDEC standard.
- DEREN PRODUCT SPEC: DR-PS-0142.

MATERIALS

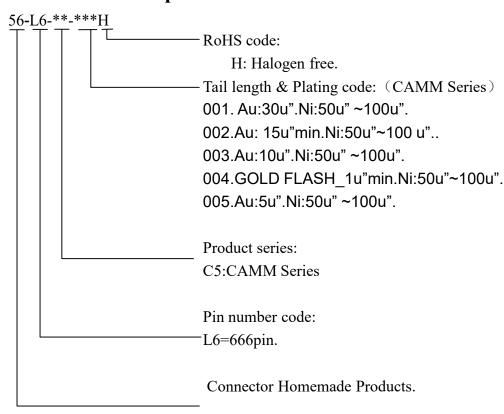
- Base Housing: LCP, UL 94 V-0.
- Contact: Copper Alloy.
- Finish: Au Plating on contact area (SEE TABLEA) [50u"~100u"] Ni plating over all.
- Back plate: stainless steel.

LPDDR5 CAMM2 LGA666 CONN PRODUCT

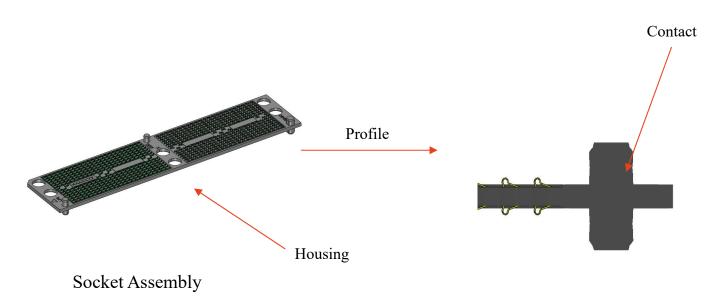


PART NUMBERS & STRUCTRUE

Part Number Description:



Product Structure



http://www.deren.com.cn E-mail: sales@deren.com Tel:0755-3326000